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| Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary) | | | | Complete if Known | |
| | | | | Application Number | NA |
| | | | | Filing Date | Herewith |
| | | | | First Named Inventor | Thomas A. Petersen Sanjay Vishin |
| | | | | Group Art Unit | |
| | | | | Examiner Name | |
| Sheet | 1 | of | 1 | Attorney Docket Number | MIPS.0187.02-00US |

| OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS | | | |
|---|-----------------------|---|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| PM | AA | JOHN KUBIATOWICZ, DAVID CHAIKEN, ANANT AGARWAL; "Closing the Window of Vulnerability in Multiphase Memory Transactions"; Laboratory for Computer Science; Massachusetts Institute of Technology; Cambridge, MA 02139; Appeared in ASPLOS V, October 1992. | |
| PM | AB | WILLIAM J. DALLY AND BRIAN TOWLES; "Route Packets, Not Wires: On-Chip Interconnection Networks"; Computer Systems Laboratory; Stanford University, Stanford, CA 94305 | |
| PM | AC | SARITA V. ADVE, KOUROSH GHARACHORLOO; "WRL Research Report 95/7; "Shared Memory Consistency Models: A Tutorial"; Western Research Laboratory, 250 University Avenue, Palo Alto, CA 94301. | |
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| Examiner Signature | /Patrick Moore/ (05/01/2006) | Date Considered | 05/01/2006 |
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